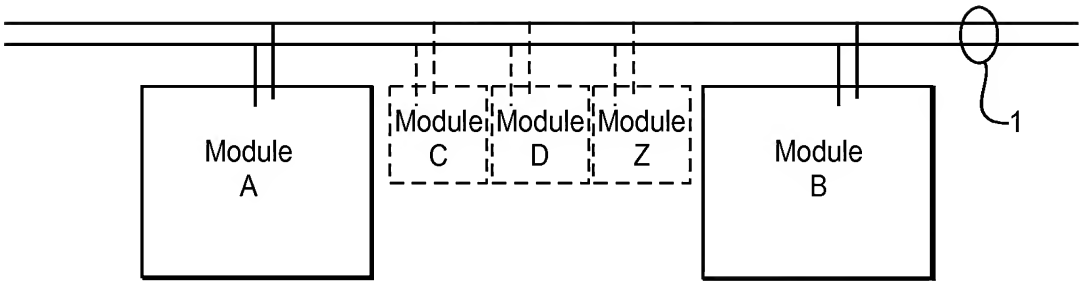
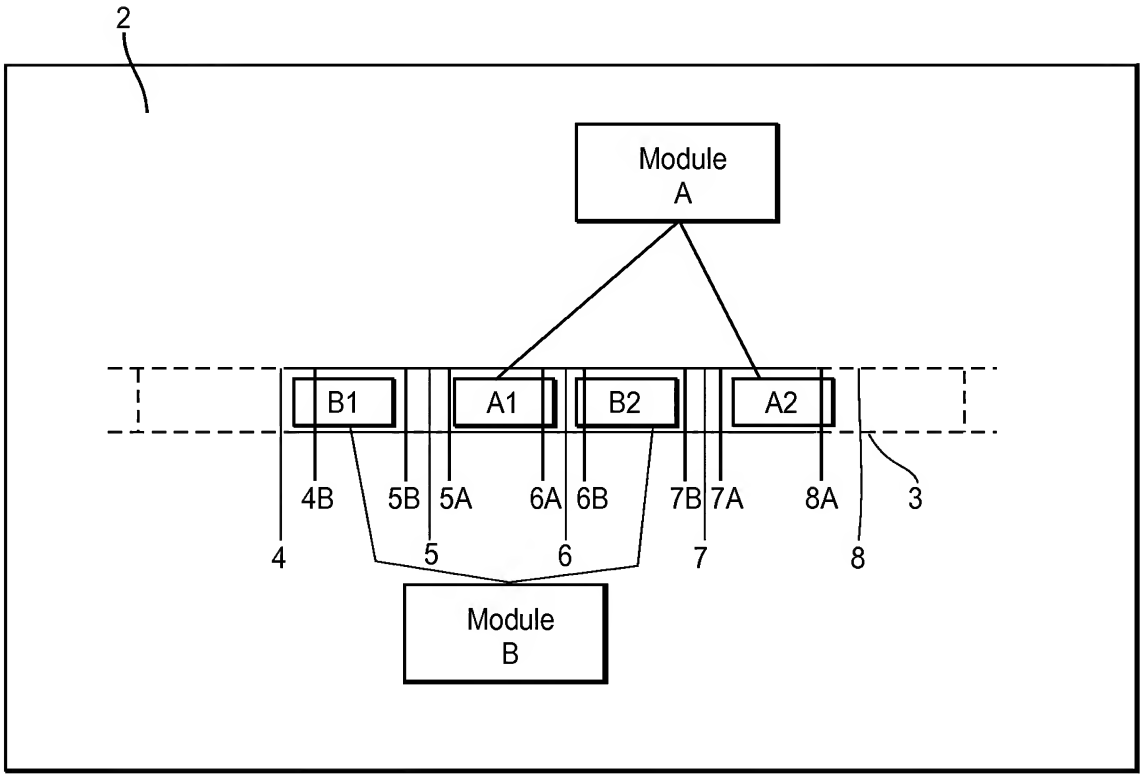
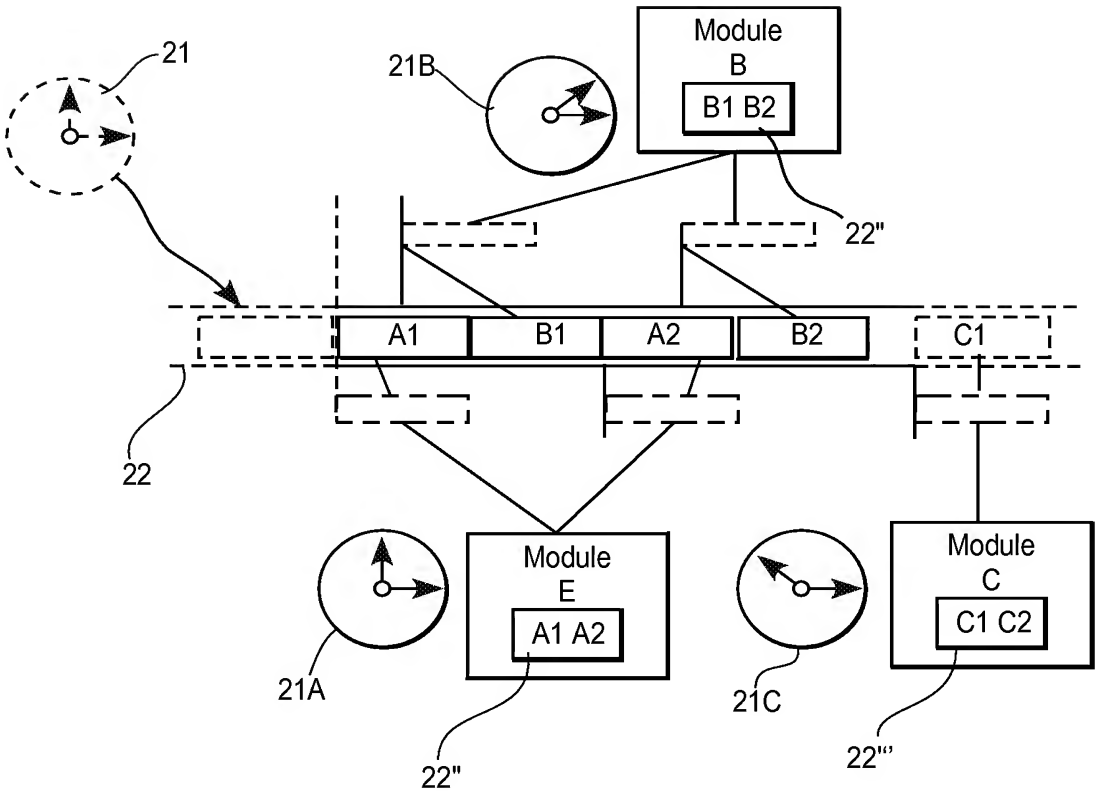
FIG. 1





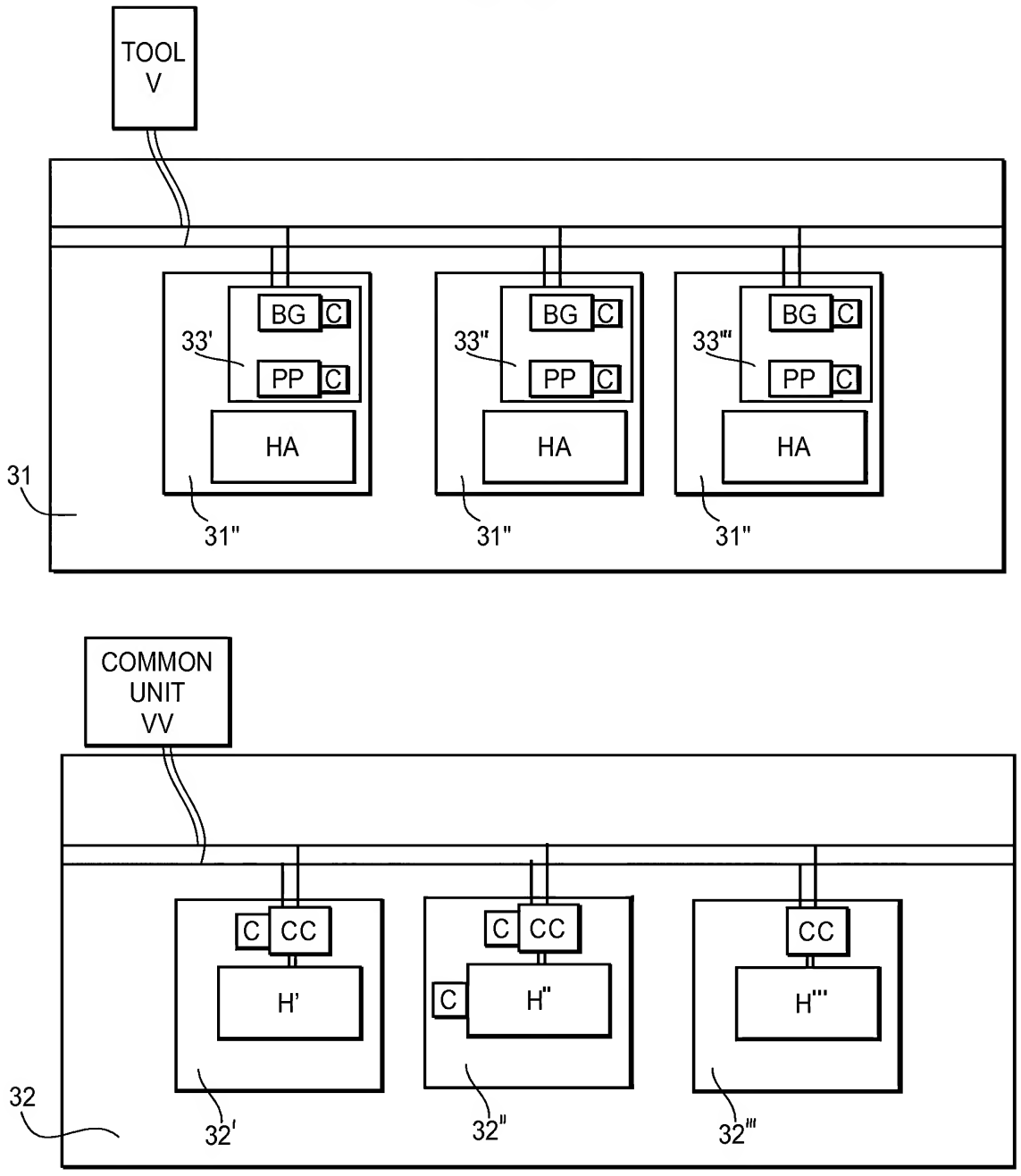
LEGEND
A1, A2, B1, B2=Messages

FIG. 2



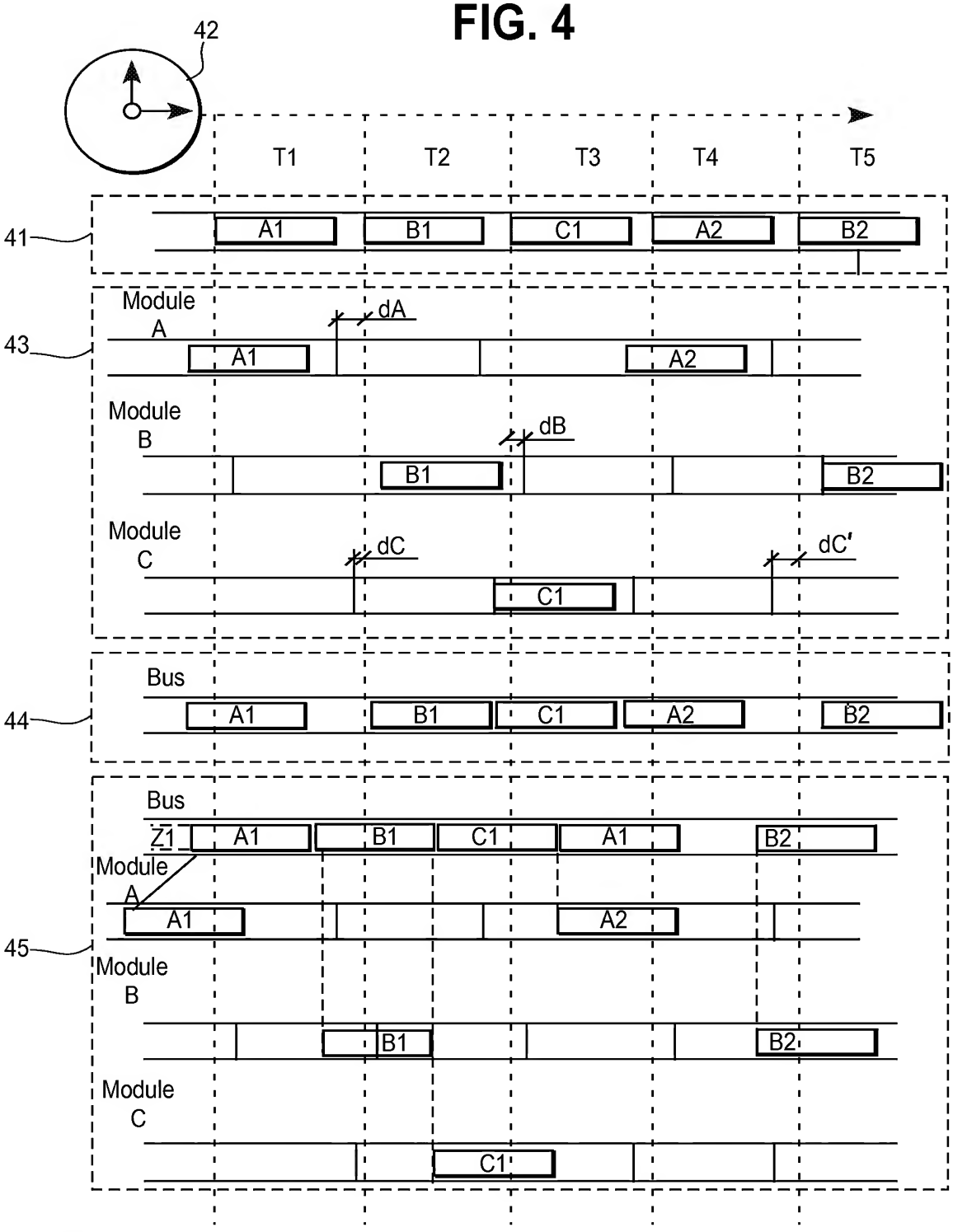
LEGEND
A1, A2, B1, B2, C1, C2=Messages

FIG. 3



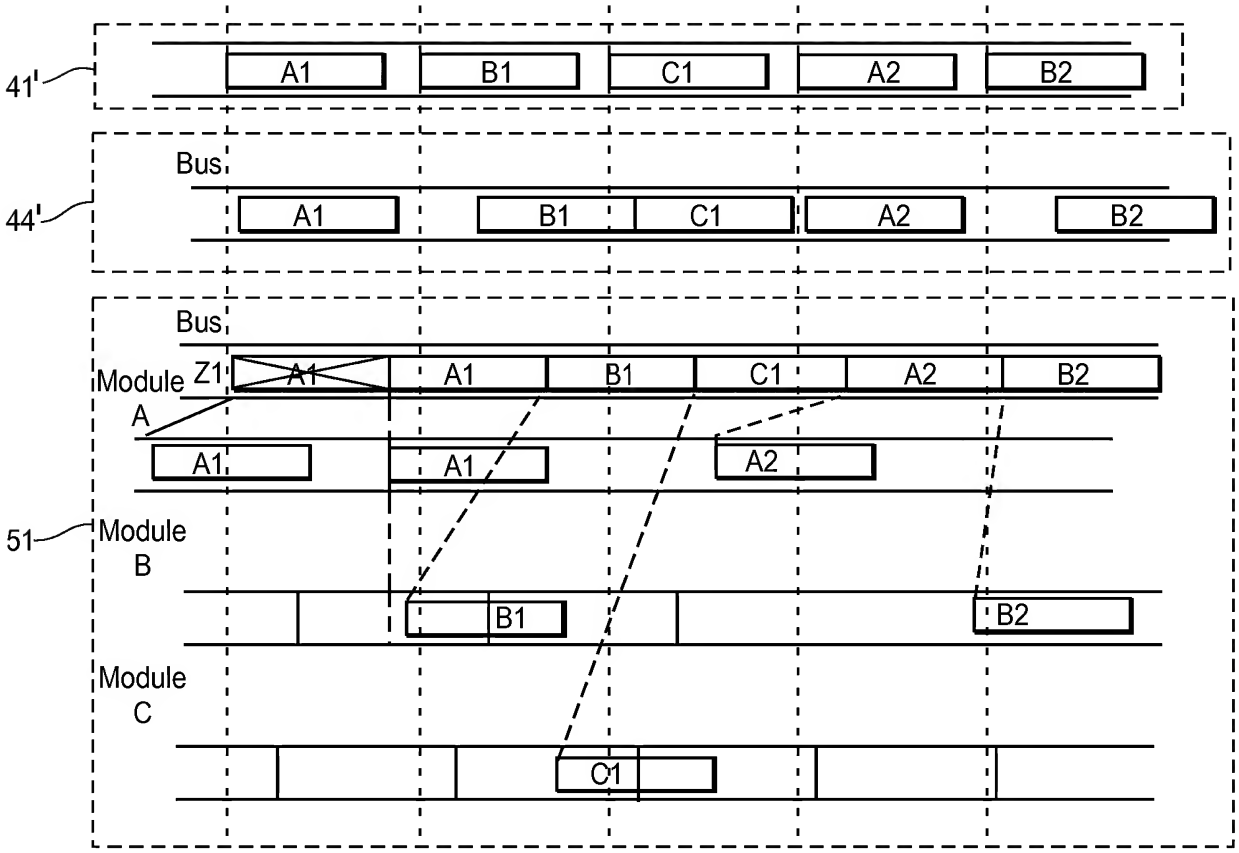
LEGEND
BG=BUS Guardian
PP=Protocol Processer
HA=Requisite Hardware
C=Clock
CC=Can Controller
H', H", H"'=Hardware

FIG. 4



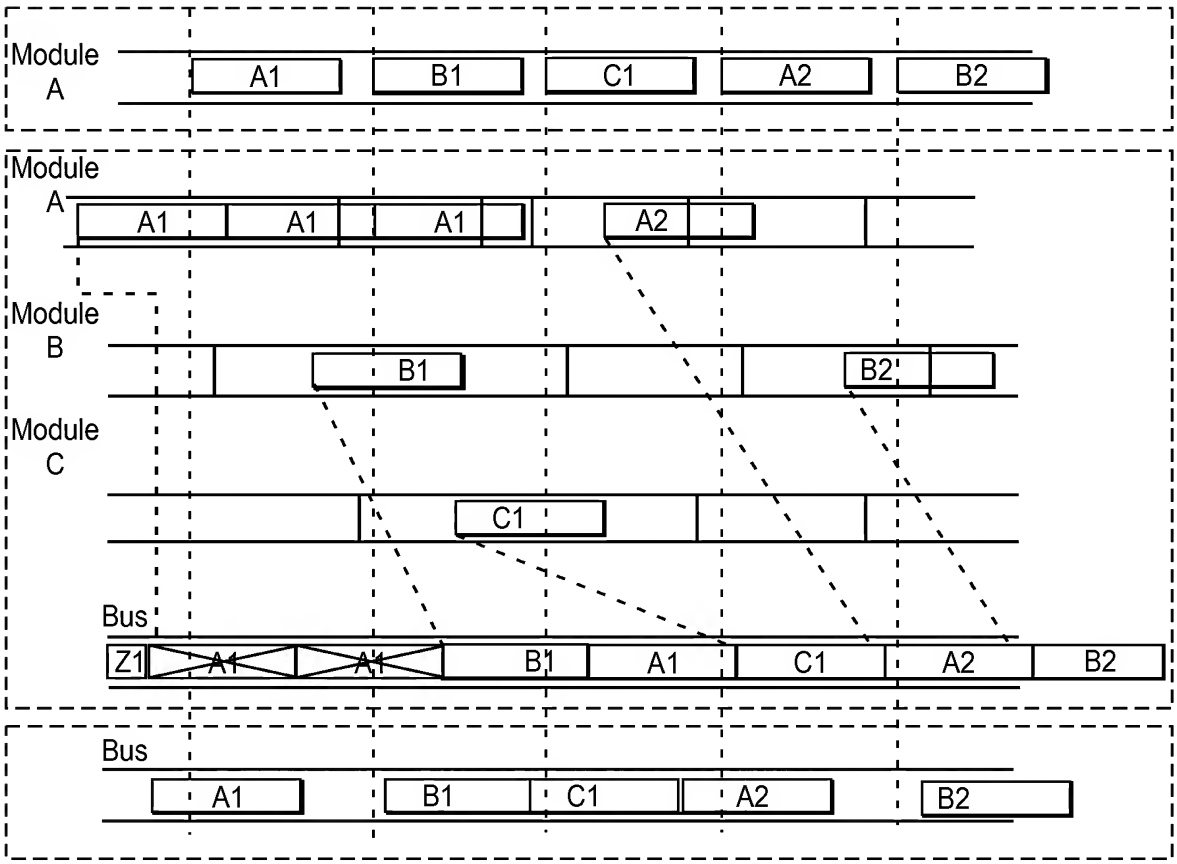
LEGEND
T1, T2, T3, T4, T5=Time slot
A1, A2, B1, B2, C1, Z1 =Message
dA=Clock delta in module A
dB=Clock delta in module B
dC=Clock delta in module C
dC'=Increased clock delta in module C

FIG. 5



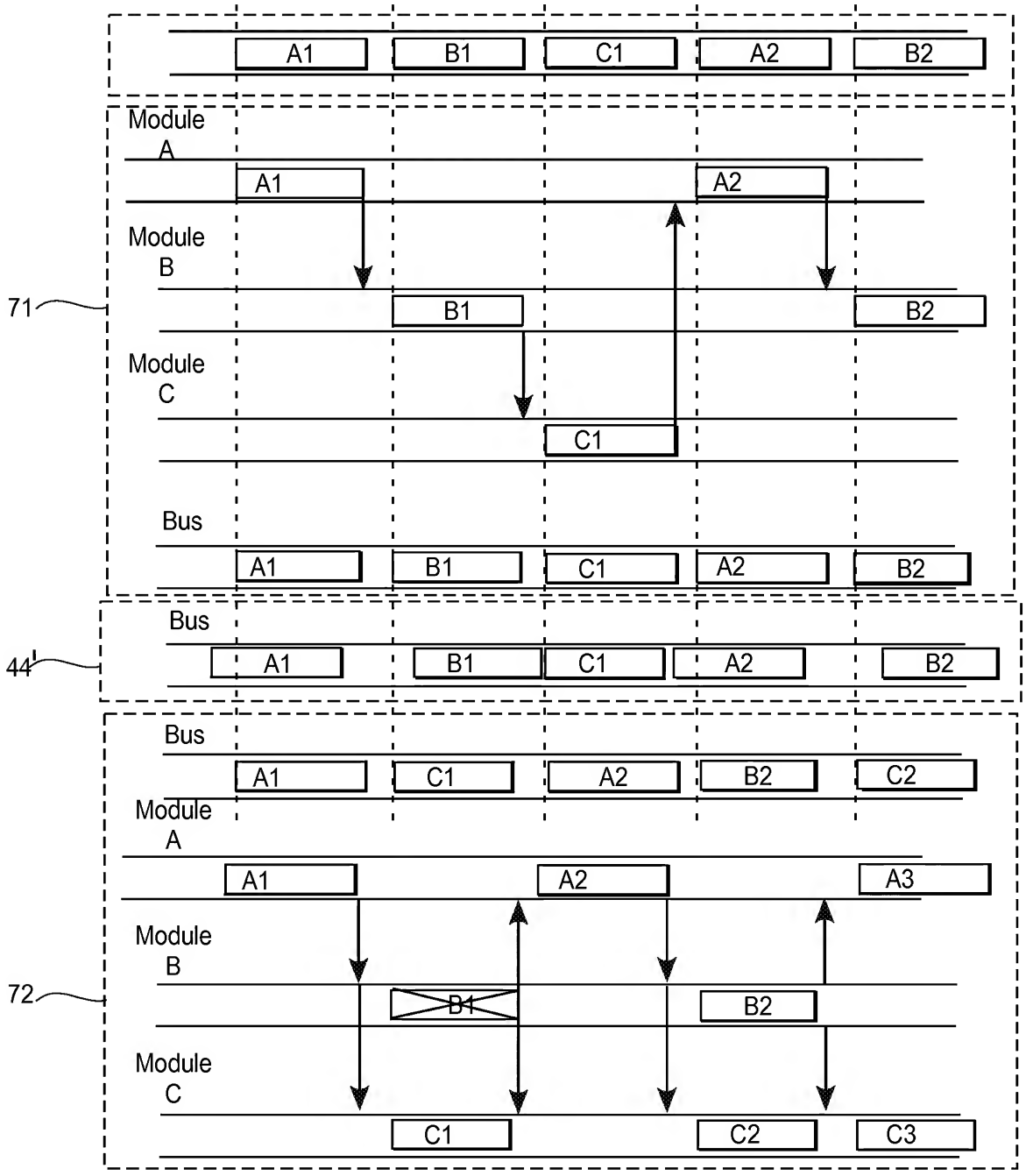
LEGEND
A1, A2, B1, B2, C1, Z1=Messages

FIG. 6



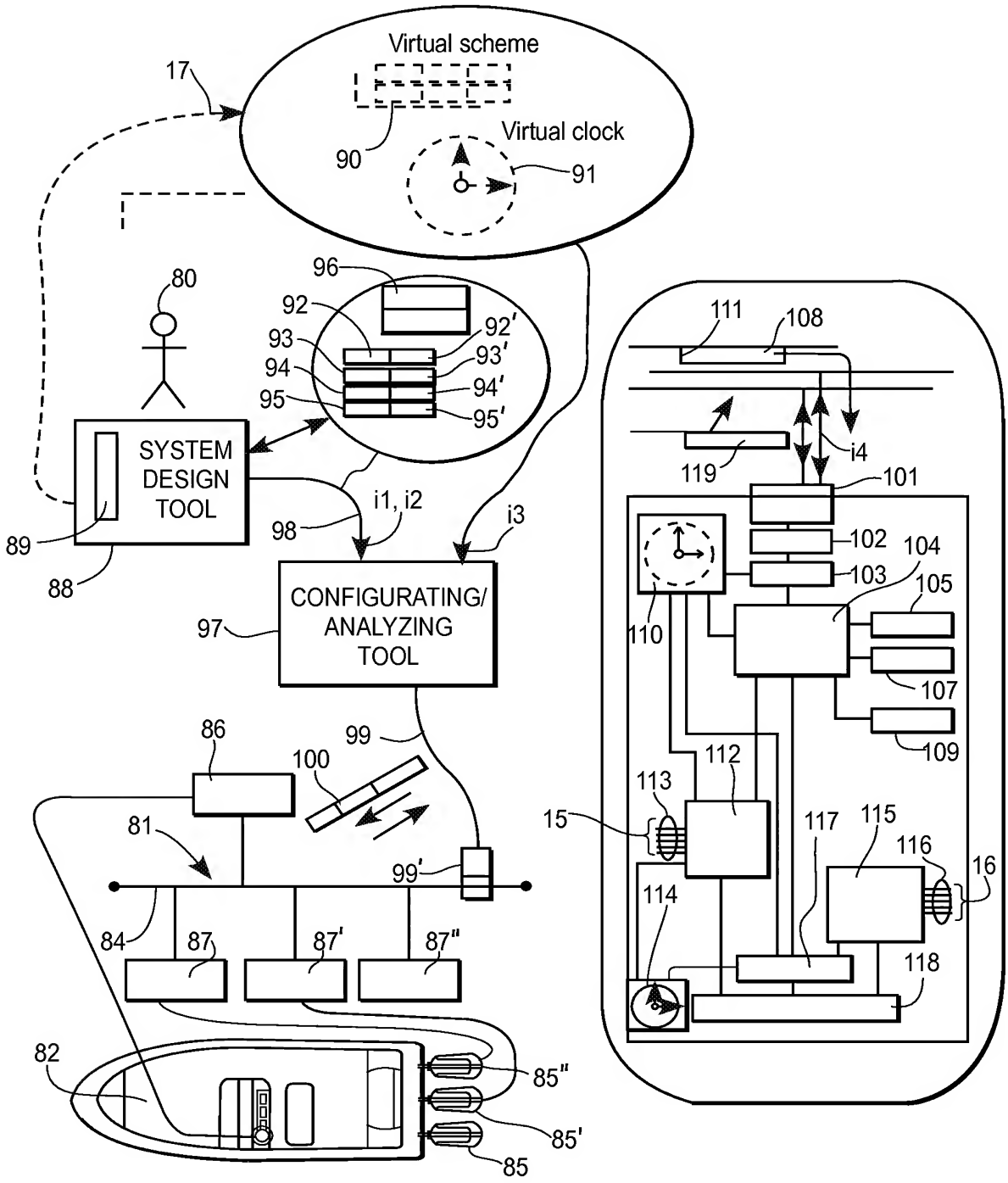
LEGEND
A1, A2, B1, C1, Z1=Messages

FIG. 7



LEGEND
A1, A2, A3, B1, B2, C1, C2, C3=Messages

FIG. 8


Virtual scheme
17
90
Virtual clock
91
80
96
92
92'
93
93'
94
94'
95
95'
SYSTEM DESIGN TOOL
89
88
98
i1, i2
i3
CONFIGURATING/ ANALYZING TOOL
97
86
100
99
81
99'
84
87
87'
87"
82
85"
85'
85
111
108
i4
119
101
102
104
103
105
110
107
109
112
113
15
117
115
116
16
114
118